

EVERVISION

Wide Temperature

DATASHEET

VGG322425-6UFLWB



Application

This specification is applied to the 3.5 inch QVGA supported TFT-LCD module, and an display 16M colors with dithering (8 bit/ color). The module is designed for PMP, GPS application and other electronic products which require flat panel display of digital signal interface.

Features

- QVGA (320×240 pixels) resolution.
- Display in 16M colors
- Line inversion mode with stripe type.
- On-chip voltage generator
- SYNC mode is supported for digital RGB input data format.
- This display has extended temperature range.

General Specifications

Item	Specifications	Unit
Screen Size	3.5 (Diagonal)	inch
Display Format	320RGB(H)×240(V)	dot
Active Area	70.08(H)×52.56(V)	mm
Pixel Pitch	0.219(H)×0.219(V)	mm
Pixel Configuration	RGB Vertical Stripe	-
Display Mode	TN Type Transmissive Mode Normally White	-
Surface Treatment	Anti-Glare and Hard Coating(3H)	-
Viewing Direction	6 O'clock (The Gray Inversion will appear at this direction)	-
Outline Dimension	76.9(W)×63.9(H)×3.3(D)	mm

Absolute Maximum Ratings

Absolute Ratings of Environment

Item	Symbol	Value		Unit
		Min.	Max.	
Storage Temperature	T _{ST}	-40	+80	°C
Operating Temperature	T _{OP}	-30	+70	°C

Electrical Absolute Ratings

TFT-LCD Module

(Ta=25±2°C, GND=V_{SS}=0V)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Digital Power Supply Voltage	VCC	-0.3	5.0	V	-

Backlight Unit

(Ta=25±2°C)

Item	Symbol	Value		Unit
		Min.	Max.	
Forward current	I _f	-	(50)	mA
Reverse voltage	V _R	-	(25)	V

Electrical Characteristics

TFT-LCD Module

(Ta=25±2°C)

Item	Symbol	Value			Unit
		Min.	Typ.	Max.	
Power Supply Voltage	V _{CC}	2.5	3.3	3.6	V
Power Supply Current	I _{CC}	-	15.6	22.0	mA
Input High Threshold Voltage	V _{IH}	0.8V _{CC}	-	V _{CC}	V
Input Low Threshold Voltage	V _{IL}	0	-	0.2V _{CC}	V
Power Consumption	P _L	-	51.48	72.6	mW
VSYNC Frequency	F _V	-	60	90	Hz
HSYNC Frequency	F _H	-	15.72	22.35	KHz
DCLK Frequency	DCLK	-	6.5	10	MHz

Backlight Unit

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
LED Voltage	VL	-	(16.5)	-	V	(1)
LED Current	IL	-	(40)	-	mA	(1)
Power Consumption	P _{BL}	-	(660)	-	mW	(1)
LED Life Time(25°C)	-	10000	30000	-	hr	(2)

Note (1) The driving design of backlight unit is dependent on serial consideration of 5S2P LEDs.

Note (2) LED life time is defined as under 25±2°C, when the average brightness decrease to 50% of original brightness

Input / Output Terminals Pin Assignment

TFT-LCD Module (CVILUX CF25541D0R0-05)

Pin No.	Symbol	I/O	Description
1	LED_K	I	LED_cathode
2	LED_K	I	LED_cathode
3	LED_A	I	LED_anode
4	LED_A	I	LED_anode
5	NC	I	No connection
6	NC	I	No connection
7	NC	I	No connection
8	RESET	I	Reset
9	NC	I	No connection
10	NC	I	No connection
11	NC	I	No connection
12	D20	I	Blue data(LSB)
13	D21	I	Blue data
14	D22	I	Blue data
15	D23	I	Blue data
16	D24	I	Blue data
17	D25	I	Blue data
18	D26	I	Blue data
19	D27	I	Blue data(MSB)
20	D10	I	Green data(LSB)
21	D11	I	Green data
22	D12	I	Green data
23	D13	I	Green data
24	D14	I	Green data
25	D15	I	Green data
26	D16	I	Green data
27	D17	I	Green data(MSB)
28	D00	I	Red data(LSB)
29	D01	I	Red data
30	D02	I	Red data

Pin No.	Symbol	I/O	Description
31	D03	I	Red data
32	D04	I	Red data
33	D05	I	Red data
34	D06	I	Red data
35	D07	I	Red data(MSB)
36	HSYNC	I	Line synchronization signal.
37	VSYNC	I	Frame synchronization signal.
38	DOTCLK	I	Dot Colck signal
39	NC	I	No connection
40	NC	I	No connection
41	V _{CC}	I	For system power supply.
42	V _{CC}	I	For system power supply.
43	NC	I	No connection
44	NC	I	No connection
45	NC	I	No connection
46	NC	I	No connection
47	NC	I	No connection
48	NC	I	No connection
49	NC	I	No connection
50	NC	I	No connection
51	NC	I	No connection
52	NC	I	No connection
53	GND	I	Ground
54	GND	I	Ground

