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4. Application

This specification is applied to the 10.1 inch WUXGA supported TFT-LCD module with projected capacitive touch (PCT), and can display true 16.7M colors (8 bit/ color). This module is composed of a 10.1" TFT-LCD panel a driver circuit and used as the input devices for general electric appliances via both finger and Capacitive stylus pen.

5. Features

- 10.1" Front IP65 Open Frame Monitor
- HDMI & VGA & DVI Interface
- Use Double Sided Tape Bonding Between PCT and LCM
- Use Double Sided Tape Bonding Between PCT and CG
- Projected Capacitive Touch
 - USB Interface
 - Multi Touch (Ten points)
 - 2048 x 2048 resolution
 - CG Chemical Strengthening
- Integrated bracket for easy installation
- Versatile mounting methods for rear mounting and VESA mounting
- Signal interface: VGA & DVI & HDMI
- Build-in 4Ω 2W*2 Speaker
- Support OSD Function
- Power connector ϕ 2.5

6. General Specifications

Item	Specifications	Unit
Screen Size	10.1 (Diagonal)	inch
Display Format	1920RGB(H)×1200(V)	dot
Active Area	216.8064(H)×135.504(V)	mm
Pixel Pitch	0.11292(H)×0.11292(V)	mm
Pixel Configuration	RGB Vertical Stripe	-
Display Mode	IPS Type/Transmissive Mode/Normally Black	-
Surface Treatment	Clear(6H)	-
Viewing Direction	Full view angle	-
Outline Dimension	249.4(W)×170.0(H)×39.0(D)	mm
Weight	(1426.8)	g
RoHS Compliance	RoHS Compliance	-

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7. Absolute Maximum Ratings

7.1 Absolute Ratings of Environment

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T _{ST}	-20	+60	°C	(1)(2)
Operating Temperature	T _{OP}	0	+50	°C	(1)(2)

Note1: Background color changes slightly depending on ambient temperature.

This phenomenon is reversible.

Note2: Please refer to item of RELIABILITY.

7.2 Electrical Absolute Ratings

7.2.1 System Power Supply

(Ta=25±2°C, GND=V_{SS}=0V)

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	VCC	10.8	13.2	V	-

8. Electrical Characteristics

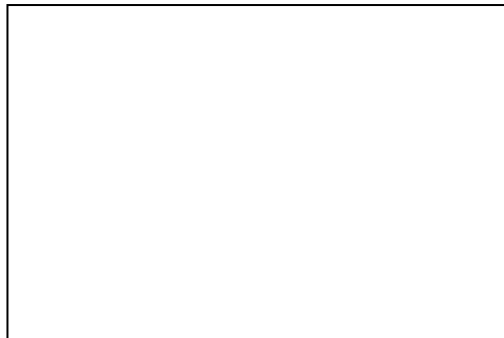
8.1 System Power Supply

(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	VCC	10.8	12.0	13.2	V	-
Power Supply Current	ICC	-	0.84	1.17	A	(1)
Power Consumption	PL	-	10.08	14.112	W	(1)
LED Life Time(25°C)	-	50000	-	-	hr	(2)

Note (1) The specified power consumption is under the conditions at VCC=12V, F_V=60Hz, whereas a power dissipation check pattern below is displayed.

White Pattern / 255 Gray



Active Area

Note (2) : LED life time is defined as under 25±2°C , when the average brightness decrease to 50% of original brightness

8.2 Projected Capacitive Touch

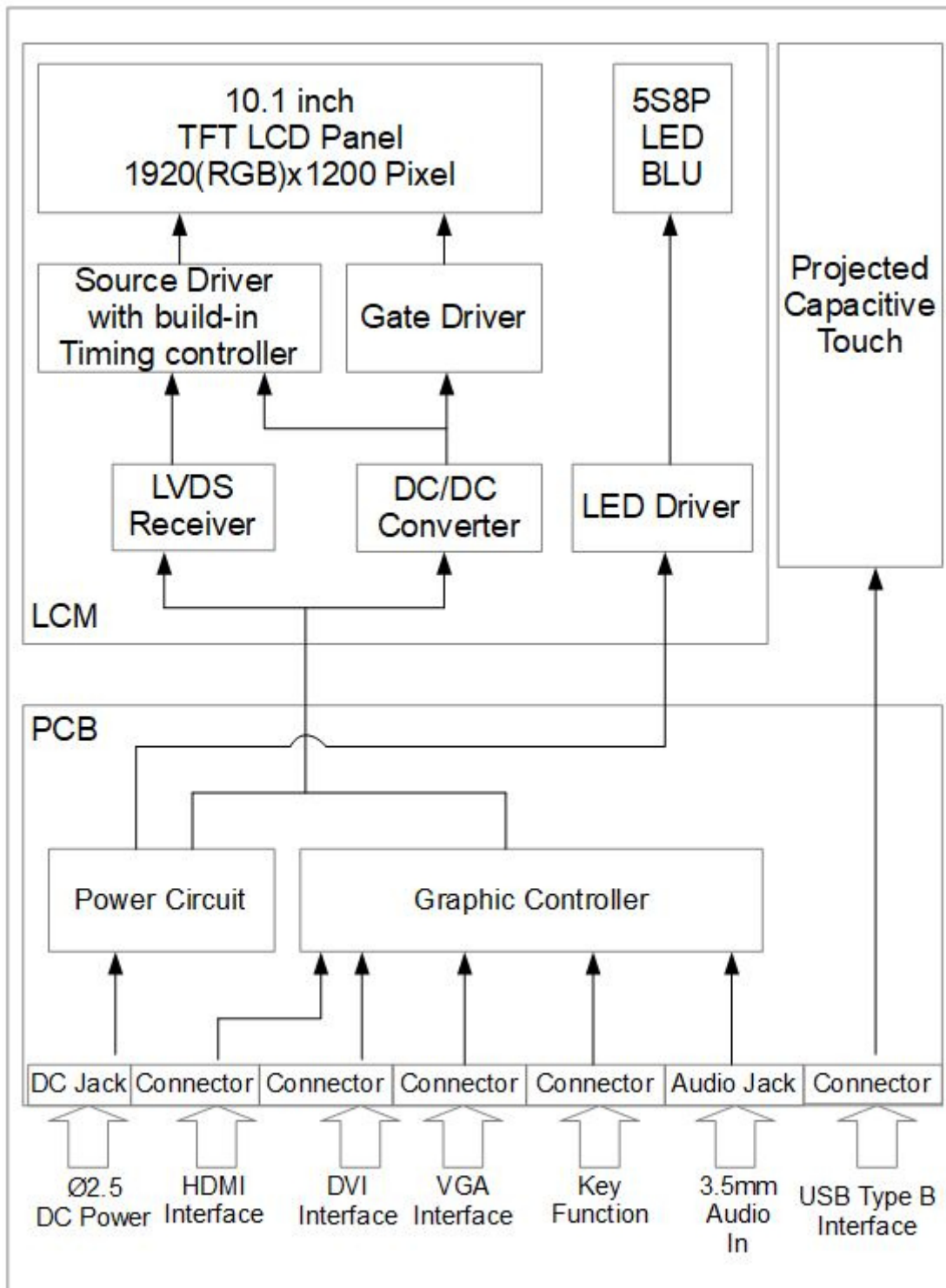
(Ta=25±2°C)

Item	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V _{TP}	4.8	5.0	5.2	V	-
Power Supply Current	I _{TP}	-	33.8	47.4	mA	(1)
Output High Threshold Voltage	V _{OH}	2.8	-	-	V	-
Output Low Threshold Voltage	V _{OL}	-	-	0.8	V	-
Differential Input Sensitivity (D+)-(D-)	V _{DI}	0.2	-	-	V	-
Differential Input Common Mode Range	V _{CM}	0.8	-	2.5	V	-
Power Consumption	P _L	-	169.0	237.0	mW	@5.0V
Report Rate	R _R	-	60	-	Hz	-
Interface		USB				-
Function		Multi Touch				-
IC Type		FT7811				-
FW Version		VIM10112-UND_10P_20210706_V5.0				-

Note (1) This test condition is touched with 10 points.

9. Block Diagram

9.1 TFT-LCD Module with Backlight Unit



10. Input / Output Terminals Pin Assignment

Connector: J1, HDMI Connector

No.	Symbol	I/O	Description
1	TMDS Data2+	I	Channel-2 positive receiver input – Positive side of channel-2 TMDS low-voltage signal differential input pair.
2	TMDS Data2 Shield	I	Ground
3	TMDS Data2–	I	Channel-2 negative receiver input – Negative side of channel-2 TMDS low-voltage signal differential input pair.
4	TMDS Data1+	I	Channel-1 positive receiver input – Positive side of channel-1 TMDS low-voltage signal differential input pair.
5	TMDS Data1 Shield	I	Ground
6	TMDS Data1–	I	Channel-1 negative receiver input – Negative side of channel-1 TMDS low-voltage signal differential input pair.
7	TMDS Data0+	I	Channel-0 positive receiver input – Positive side of channel-0. TMDS low-voltage signal differential input pair.
8	TMDS Data0 Shield	I	Ground
9	TMDS Data0–	I	Channel-0 negative receiver input – Negative side of channel-0. TMDS low-voltage signal differential input pair.
10	TMDS Clock+	I	Clock positive receiver input – Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	I	Ground
12	TMDS Clock–	I	Clock negative receiver input – Negative side of reference clock. TMDS low-voltage signal differential input pair
13	N.C.	-	Not Connection
14	N.C.	-	Not Connection

No.	Symbol	I/O	Description
15	HDMI_DDC_SCL	I	Serial Clock
16	HDMI_DDC_SDA	I	Serial Data
17	N.C.	-	Not Connection
18	HDMI 5V Power	I	5V
19	HDMI_HPD	O	Hot Plug Detect

Connector: J3, DVI Connector

No.	Symbol	I/O	Description
1	TMDS Data2-	I	Channel-2 negative receiver input – Negative side of channel-2 TMDS low-voltage signal differential input pair.
2	TMDS Data2+	I	Channel-2 positive receiver input – Positive side of channel-2 TMDS low-voltage signal differential input pair.
3	TMDS Data2 Shield	I	Ground
4	NC	-	Not Connection
5	NC	-	Not Connection
6	DVI_DDC_SCL	I	Serial Clock
7	DVI_DDC_SDA	I	Serial Data
8	NC	-	Not Connection
9	TMDS Data1-	I	Channel-1 negative receiver input – Negative side of channel-1 TMDS low-voltage signal differential input pair.
10	TMDS Data1+	I	Channel-1 positive receiver input – Positive side of channel-1 TMDS low-voltage signal differential input pair.
11	TMDS Data1 Shield	I	Ground
12	NC	-	Not Connection
13	NC	-	Not Connection

No.	Symbol	I/O	Description
14	DVI 5V Power	I	5V
15	GND	I	Ground
16	DVI_HPDP	O	Hot Plug Detect
17	TMDS Data0-	I	Channel-0 negative receiver input – Negative side of channel-0. TMDS low-voltage signal differential input pair.
18	TMDS Data0+	I	Channel-0 positive receiver input – Positive side of channel-0. TMDS low-voltage signal differential input pair.
19	TMDS Data0 Shield	I	Ground
20	NC	-	Not Connection
21	NC	-	Not Connection
22	TMDS Clock Shield	I	Ground
23	TMDS Clock+	I	Clock positive receiver input –Positive side of reference clock. TMDS low-voltage signal differential input pair
24	TMDS Clock-	I	Clock negative receiver input – Negative side of reference clock. TMDS low-voltage signal differential input pair
C1	NC	-	Not Connection
C2	NC	-	Not Connection
C3	NC	-	Not Connection
C4	NC	-	Not Connection
C5	GND	I	Ground

Connector: J2, VGA Connector

No.	Symbol	I/O	Description
1	Red	I	Red Signal
2	Green	I	Green Signal
3	Blue	I	Blue Signal
4	NC	-	Not Connection
5	GND	I	Ground
6	Red_Return	I	Ground
7	Green_Return	I	Ground
8	Blue_Return	I	Ground
9	VGA 5V Power	I	5V
10	GND	I	Ground
11	NC	-	Not Connection
12	DVI_DDC_SDA	I	Serial Data
13	VGA_HS	I	Horizontal Signal
14	VGA_VS	I	Vertical Signal
15	DVI_DDC_SCL	I	Serial Clock

Connector: J8, USB Type B Connector

No.	Symbol	I/O	Description
1	V _{TP}	I	+5.0V USB Power Supply Input
2	D-	I	USB D- Signal
3	D+	I	USB D+ Signal
4	GND	I	Ground

11. Interface Timing

11.1 Applicable Format

Input Signal	Resolution
VGA	640x480@60Hz
	800x600@60Hz
	1024x768@60Hz
	1280x800@60Hz
	1920x1080@60Hz
	1920x1200@60Hz
DVI	640x480@60Hz
	800x600@60Hz
	1024x768@60Hz
	1280x800@60Hz
	1920x1080@60Hz
	1920x1200@60Hz
HDMI	640x480@60Hz
	800x600@60Hz
	1024x768@60Hz
	1280x800@60Hz
	1920x1080@60Hz
	1920x1200@60Hz

11.2 USB Interface

The Multi Touch Function works with plug'n play under system Windows 7 or later.

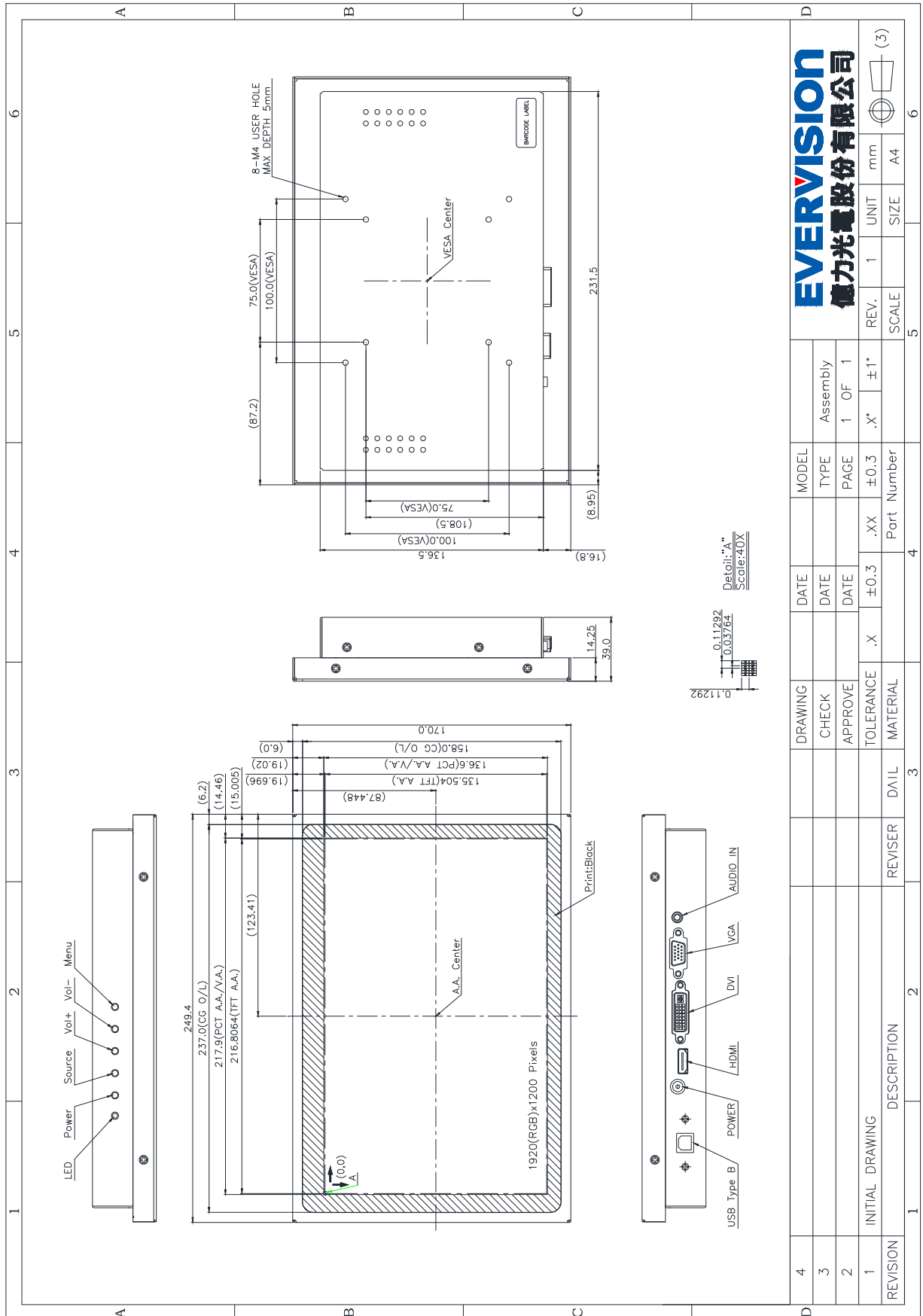
For older Windows systems or other operating systems a driver must be programmed.

12. Optical Characteristics

The optical characteristics should be measured in a dark environment (≤ 1 lux) or equivalent state with the methods shown in Note (4).

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR		700	(900)	-	-	(2)
Response Time		T_{R+T_F}		-	25	-	ms	(3)
Luminance(Center)		Y		660	(790)	-	cd/m ²	(4)
Brightness uniformity		BUNI		75	-	-	%	(5)
Color Chromaticity	White	Wx		$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing Normal Angle	0.235	0.285	0.335	-
		Wy	0.287		0.327	0.377	-	
	Blue	Bx	0.107		0.157	0.207		
		By	0.004		0.054	0.104		
	Green	Gx	0.265		0.315	0.365		
		Gy	0.582		0.632	0.682		
	Red	Rx	0.594		0.644	0.694		
		Ry	0.294		0.344	0.394		
Viewing Angle	Horizontal	θ_{x+}	CR \geq 10	80	(85)	-	deg.	
		θ_{x-}		80	(85)	-		
	Vertical	θ_{y+}		80	(85)	-		
		θ_{y-}		80	(85)	-		

16.Outline Drawing



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REVISION	DESCRIPTION	REVISOR	DATE	MATERIAL	TOLERANCE	APPROVE	CHECK	DRAWING	DATE	MODEL	TYPE	PAGE	OF	ASSEMBLY
1	INITIAL DRAWING				.X ±0.3							1	1	Assembly
2					.XX ±0.3									
3					.X ±1*									
4														

REV.	UNIT	SCALE	SIZE
1	mm		A4

Part Number	Port Number
	5

MODEL	TYPE	PAGE	OF	ASSEMBLY
		1	1	Assembly

DATE	DATE	DATE

DATE	DATE	DATE

DATE	DATE	DATE

DATE	DATE	DATE

DATE	DATE	DATE

DATE	DATE	DATE

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